

Figures

Figure 1: A schematic diagram illustrating the experimental setup for measuring the time delay of a signal. The diagram shows a signal source (S) connected to a delay line (DL) and a detector (D). The signal source is represented by a square wave, and the delay line is represented by a series of rectangular blocks. The detector is represented by a square wave. The time delay is indicated by the time interval between the signal source and the detector.

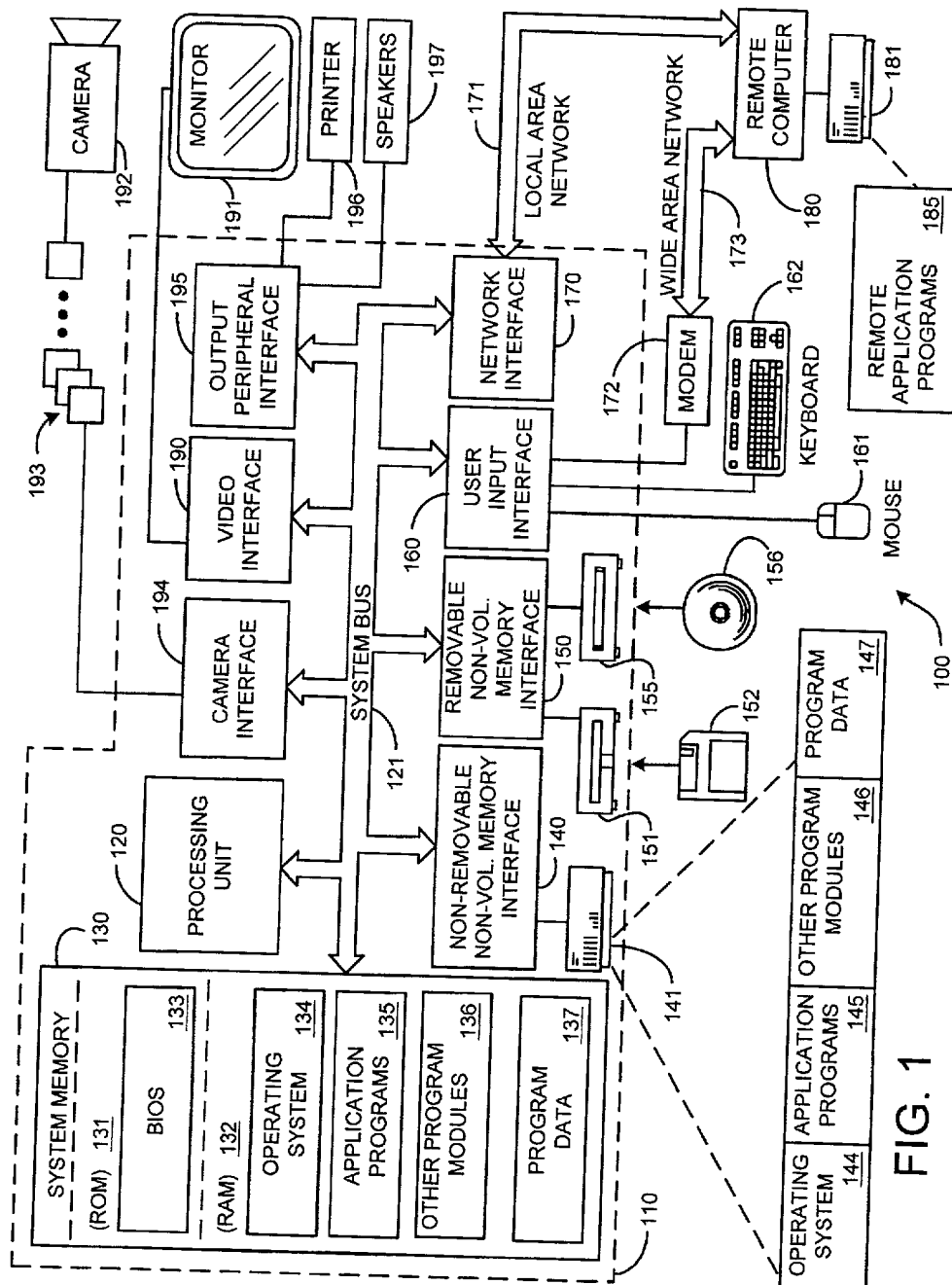


FIG. 1

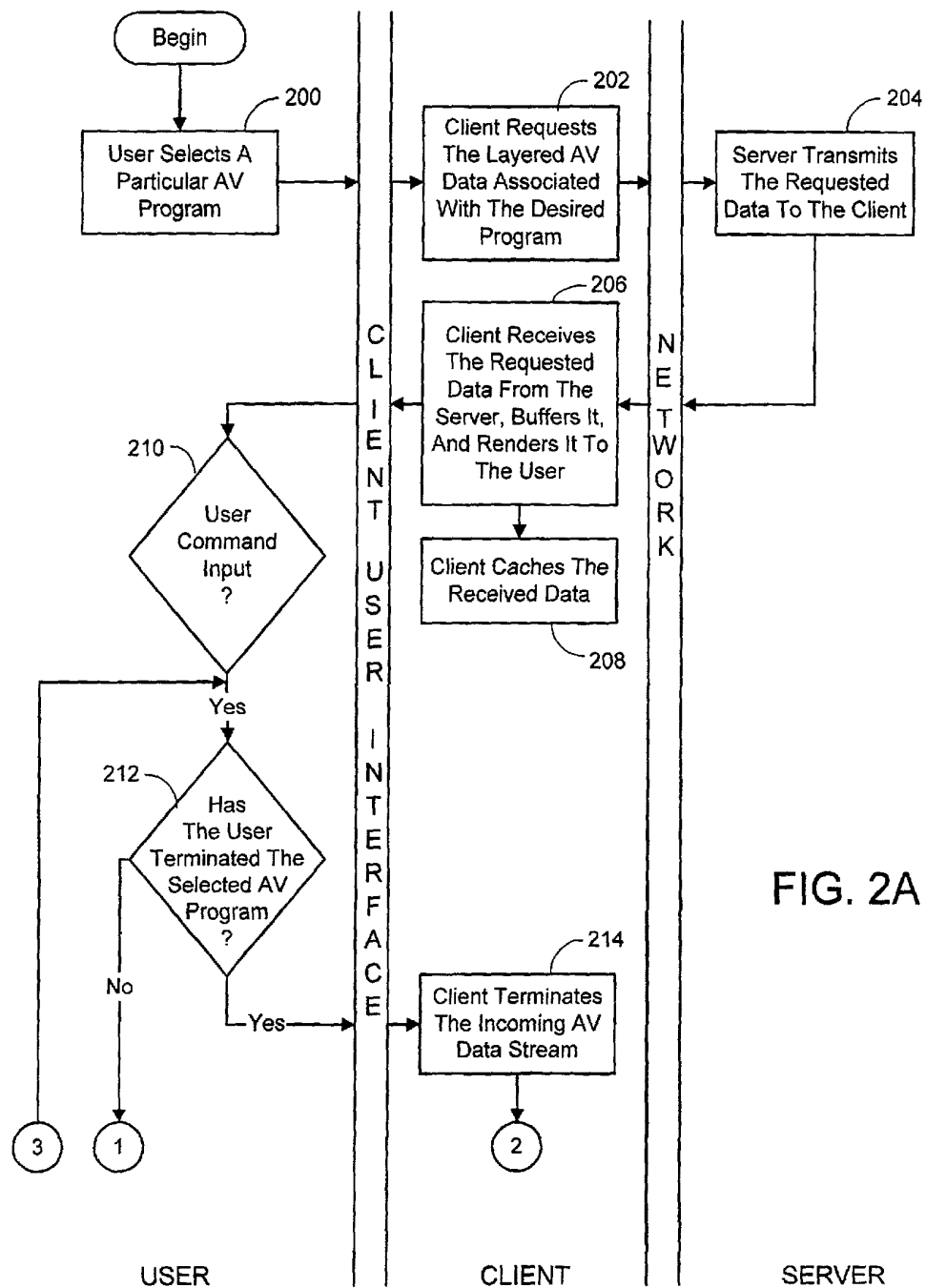


FIG. 2A

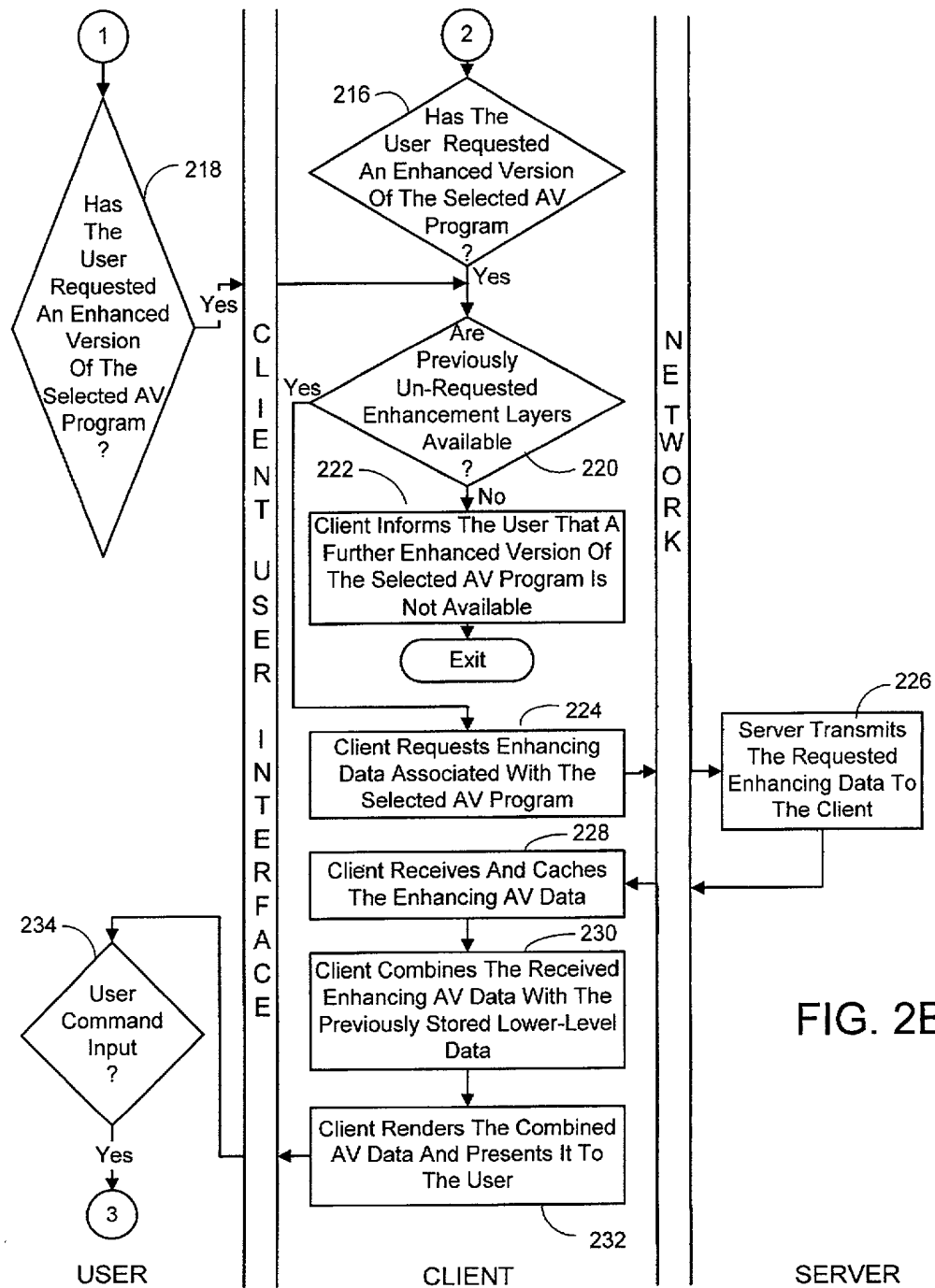


FIG. 2B

